

INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.3268	Application No.	
Applicants	Seiji INUMIYA et al.		
Filing Date	March 12, 2004	Group:	

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
<i>Wb</i>		6,020,243	02/01/2000	Wallace et al.			

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
<i>W</i>		2000-49349	02/18/2000	Japan			No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
<i>W</i>	S. INUMIYA, et al. "Method of Manufacturing Semiconductor Device", U.S. Application No. 10/101,913, filed March 21, 2002.

Examiner	<i>R. Kumar</i>	Date Considered	<i>2/28/05</i>
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		
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